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Application No : 09/780,737 Amendment dated: August 20, 2004 Reply to Office Action of May 5, 2004

Listing of Claims:

(Currently amended) A method of addressing a bistable cholesteric liquid crystal material having incremental reflectance properties disposed between opposed substrates, wherein one substrate has a first plurality of electrodes deposited thereon facing the other substrate which has a second plurality of electrodes disposed thereon, the intersection of the first and second plurality of electrodes forming a plurality of pixels, the addressing method comprising.

applying a predetermined number of pulses to the first plurality of electrodes within a set period of time, each said pulse applied to the first electrodes having a different duration drive period within said set period of time;

applying a like number of said predetermined number of pulses to the second plurality of electrodes, which is the same number of pulses as applied to said first plurality of electrodes, within said set period of time, each said pulse applied to the second electrodes also having said different duration drive periods within said set period of time; and

selectively associating one of two amplitude values with at least one of said predetermined number of pulses applied to the electrodes to generate a desired incremental reflectance for each of the pixels, wherein said desired incremental reflectance is determined by which one of said amplitude values is associated with which one of said different duration drive periods.

(Cancelled)

3. (Previously amended) The method according to claim 1, further comprising: preparing said liquid crystal material by applying a preparation pulse to the first and second plurality of electrodes, prior to said applying steps Application No. 09/780,737 Amendment dated August 20, 2004 Reply to Office Action of May 5, 2004

- 4. (Currently amended) The method according to claim 1, wherein each of said different drive duration periods are applied to the first and second plurality of electrodes at the same time.
- 1 5 (Previously amended) The method according to claim 1, wherein the number of said predetermined number of pulses correspond to a different number of said desired incremental reflectances
- 1 6 (Previously amended) The method according to claim 1, wherein a number of said
 2 desired incremental reflectances at each pixel is equal to two raised to the number
 3 of said predetermined number of pulses less one, or less a constant value.
- 7. (Previously amended) The method according to claim 1, wherein said pulses are bipolar
- 1 8. (Previously amended) The method according to claim 1, wherein said pulses are unipolar.
- 9. (Previously amended) The method according to claim 1, wherein the number of said predetermined number of pulses is equal to a number of said desired incremental reflectances.
- 10. (Previously amended) The method according to claim 9, wherein said number of said desired incremental reflectances corresponds to said number of drive periods, each said drive period having a different length of time than all other said drive periods.
- 1 11. (Previously amended) The method according to claim 1, wherein said number of said predetermined number of pulses is equal to an exponent number applied to two, wherein the exponent number corresponds to a number of pulses, plus one, or plus a constant value.

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- 12. (Currently amended) The method according to claim 11, wherein said exponent
 number of pulses corresponds to said different duration drive periods, each said
 different duration drive period having a different length of time, and wherein the
 additional pulse corresponds to a preparation pulse.
- 13. (Original) The method according to claim 12, wherein the shortest drive period is about half the duration of the next longest drive period.
- 1 14. (Original) The method according to claim 12, wherein each drive period is at least
 2 either about twice as long in duration as the next shortest drive period or about half
 3 as short in duration as the next longest drive period.
 - 15. (Currently amended) A liquid crystal display, comprising:

a pair of opposed substrates having disposed therebetween a cholesteric liquid crystal material, one of said substrates having a first plurality of electrodes disposed thereon facing the other of said substrates which has a second plurality of electrodes, wherein the intersection of said first and second plurality of electrodes form a plurality of pixels; and

a drive circuit that applies a predetermined number of pulses to said first plurality of electrodes and a like said predetermined number of pulses to said second plurality of electrodes, which is the same number of pulses as applied to said first plurality of electrodes, within a set period of time, each of said predetermined number of pulses having a different duration drive period within said set period of time, said drive circuit associating one of two amplitude values with at least one of said predetermined number of pulses to generate a desired incremental reflectance for each of the pixels which is determined by which one of said amplitude values is associated with which one of said different duration drive periods.

16. (Cancelled)

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1	17	(Currently amended) The liquid crystal display according to claim 15, wherein
2		said drive circuit applies each of said different duration drive periods to said first
3		and second plurality of electrodes at the same time

- 18. (Previously amended) The liquid crystal display according to claim 15, wherein the number of said predetermined number of pulses correspond to a different number of incremental reflectances
- 19. (Previously amended) The liquid crystal display according to claim 15, wherein the number of said predetermined number of pulses is equal to a number of incremental reflectances.

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